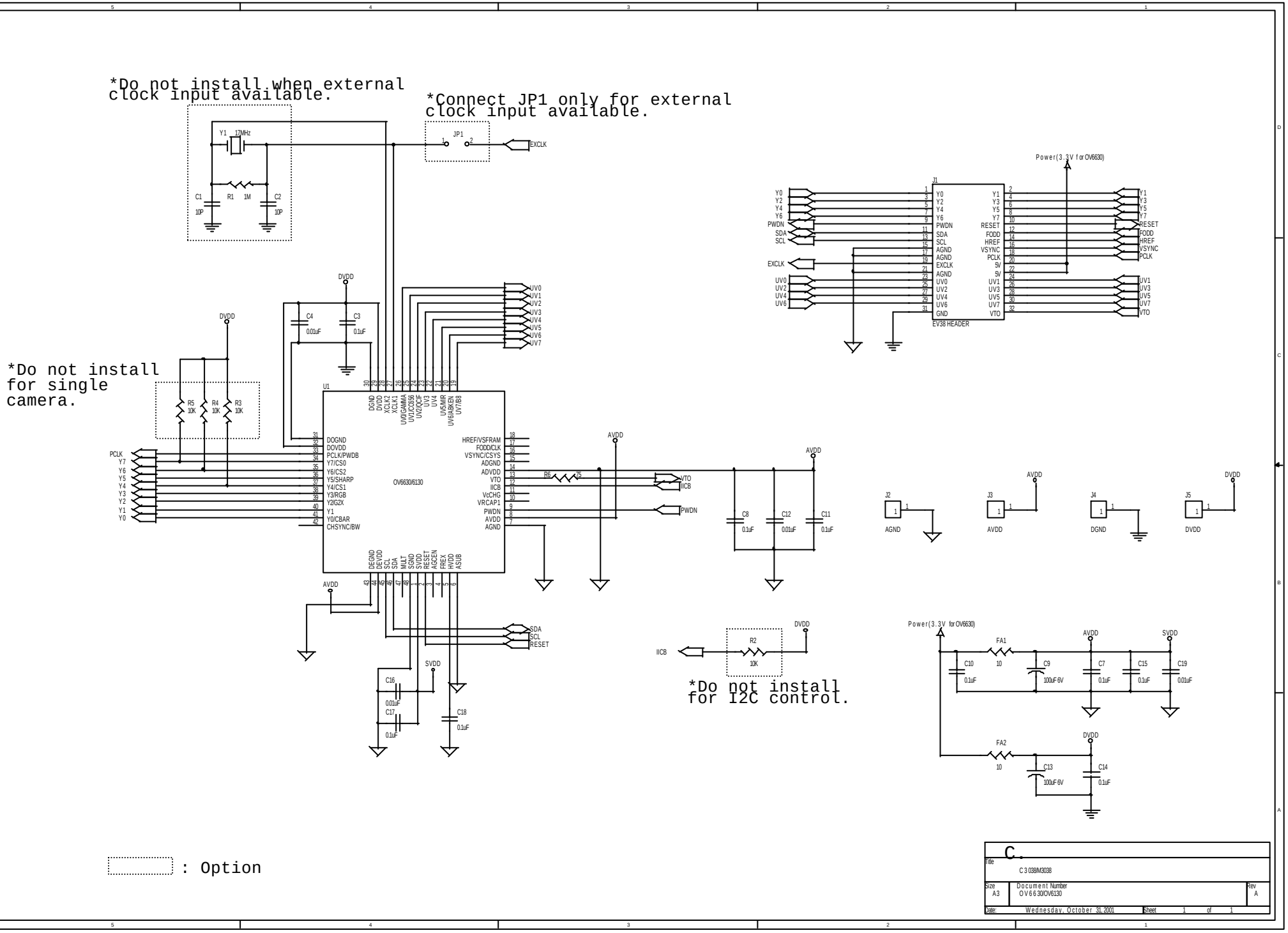
*Do not install when external clock input available.
*Connect JP1 only for external clock input available.
Y1 17MHz
R1 1M
C1 10P
C2 10P
JP1
EXCLK
Power(3.3V for OV6630)
J1
EV38 HEADER
Y0
Y2
Y4
Y6
PWDN
SDA
SCL
EXCLK
UV0
UV2
UV4
UV6
GND
Y1
Y3
Y5
Y7
RESET
FODD
HREF
VSYNC
PCLK
5V
UV1
UV3
UV5
UV7
VTO
DVDD
C4 0.01uF
C3 0.1uF
UV0
UV1
UV2
UV3
UV4
UV5
UV6
UV7
*Do not install for single camera.
R5 10K
R4 10K
R3 10K
U1
OV6630/6130
DGND
DVDD
XCLK2
XCLK1
UV0/GAMMA
UV1/CC656
UV2/CC/F
UV3
UV4
UV5/MIR
UV6/ABKEN
UV7/B8
PCLK
Y7/CS0
Y6/CS2
Y5/SHARP
Y4/CS1
Y3/RGB
Y2/GZX
Y1
Y0/CBAR
CHSYNC/BW
HREF/VSFRAM
FODD/CLK
VSYNC/CSYS
ADGND
ADVDD
VTO
IICB
VcCHG
VRCAP1
PWDN
AVDD
AGND
DEGND
DEVDD
SCL
SDA
MULT
SGND
SVDD
RESET
AGEN
FREX
HVDD
ASUB
AVDD
R6
R5
VTO
IICB
PWDN
C8 0.1uF
C12 0.01uF
C11 0.1uF
J2
AGND
J3
AVDD
J4
DGND
J5
DVDD
SDA
SCL
RESET
SVDD
C16 0.01uF
C17 0.1uF
C18 0.1uF
IICB
R2 10K
DVDD
*Do not install for I2C control.
Power(3.3V for OV6630)
FA1 10
C10 0.1uF
C9 100uF-6V
C7 0.1uF
C15 0.1uF
C19 0.01uF
AVDD
SVDD
FA2 10
C13 100uF-6V
C14 0.1uF
DVDD
: Option
C
Title
C3038/M3038
Size A3
Document Number
OV6630/OV6130
Rev A
Date: Wednesday, October 31, 2001
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